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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/791,629	03/03/2004	Soo-Chan Lee	2421-000033/US	3096	
75	90 01/05/2006	EXAMINER			
HARNESS, DICKEY & PIERCE, P.L.C.			HOLLINGTON	HOLLINGTON, JERMELE M	
P.O. BOX 8910 RESTON, VA 20195		•	ART UNIT	PAPER NUMBER	
			2829		
			DATE MAILED: 01/05/200	DATE MAILED: 01/05/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

8g

	Application No.	Applicant(s)					
000-000	10/791,629	LEE ET AL.					
Office Action Summary	Examiner	Art Unit					
	Jermele M. Hollington	2829					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1) Responsive to communication(s) filed on 03 M	<u> March 2004</u> .						
£4/							
3) Since this application is in condition for allows							
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D. 11, 4	53 U.G. 213.					
Disposition of Claims							
4) Claim(s) 1-42 is/are pending in the application.							
4a) Of the above claim(s) is/are withdra	awn from consideration.						
5) Claim(s) is/are allowed.							
6) Claim(s) is/are rejected.							
	7) ☐ Claim(s) is/are objected to. 8) ☑ Claim(s) <u>1-42</u> are subject to restriction and/or election requirement.						
b) Claim(3) 1-42 are subject to reciment and a	,	•					
Application Papers	· ,						
9) The specification is objected to by the Examin	ier.	Everiner					
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority under 35 U.S.C. § 119		-\ (d) a. (D					
12) Acknowledgment is made of a claim for foreig	n priority under 35 U.S.C. § 119(a)-(a) or (ī).					
a) All b) Some * c) None of:							
 Certified copies of the priority documents have been received. Certified copies of the priority documents have been received in Application No 							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)).							
* See the attached detailed Office action for a list of the certified copies not received.							
Attachment(s) 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)							
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)							
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 5) Notice of Informat Patent Application (PTO-152) 6) Other:							

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DETAILED ACTION

Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:

- Claims 1-10, 33-37 and 40-42, drawn to semiconductor device test apparatus with a stacker and robot, classified in class 324, subclass 765.
- II. Claims 11-32 and 38-39, drawn to semiconductor device test apparatus with socket assembly and heat sink, classified in class 324, subclass 763.

The inventions are distinct, each from the other because of the following reasons:

- 2. Inventions I and II are related as subcombinations disclosed as usable together in a single combination. The subcombinations are distinct from each other if they are shown to be separately usable. In the instant case, Invention I has separate utility such as a stacker for stacking devices before and after a test as well as a robot and Invention II has separate utility such as a socket assembly having a socket block and plurality of socket guides as well as a lead pusher assembly. See MPEP § 806.05(d).
- Because these inventions are distinct for the reasons given above and the search required for Group I is not required for Group II, restriction for examination purposes as indicated is proper.
- 4. Applicants are advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).
- 5. Applicants are reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the

currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jermele M. Hollington whose telephone number is (571) 272-1960. The examiner can normally be reached on M-F (9:00-4:30 EST) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nestor Ramirez can be reached on (517) 272-2034. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jermele M. Hollington Patent Examiner Art Unit 2829

JMH June 17, 2005



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10/791,629

APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION		ATTORNEY DOCKET NO.
				EXAMINER

ART UNIT PAPER

20050617

DATE MAILED:

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Commissioner for Patents

Due to a mistake by the office, new Office Action is given with restarting the time peiod of response.

Jermele M. Hollington Primary Examiner

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